

F 0 S T F 8 0 " 6 X E D E 6 5 0

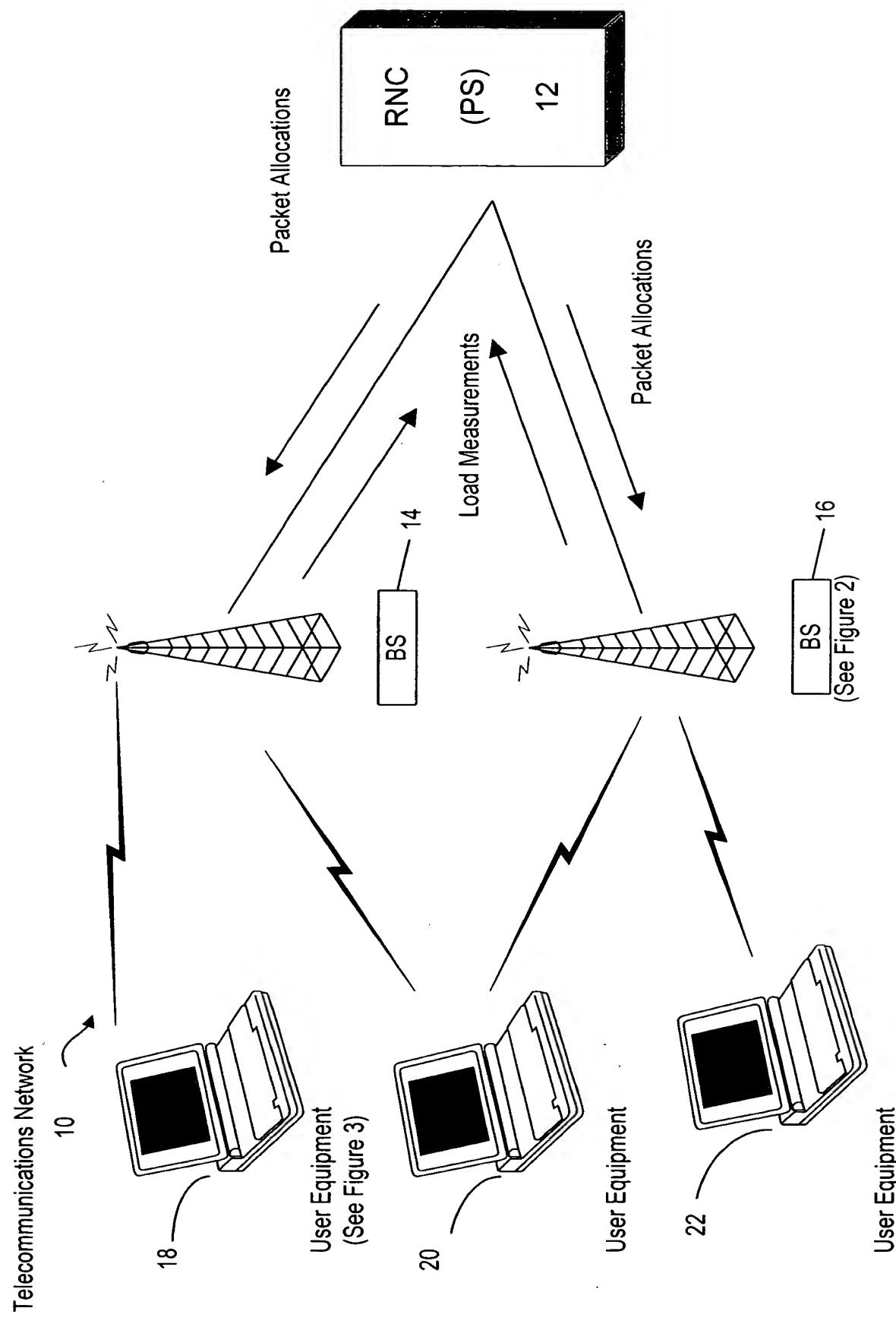


Figure 1 (The Basic Invention)

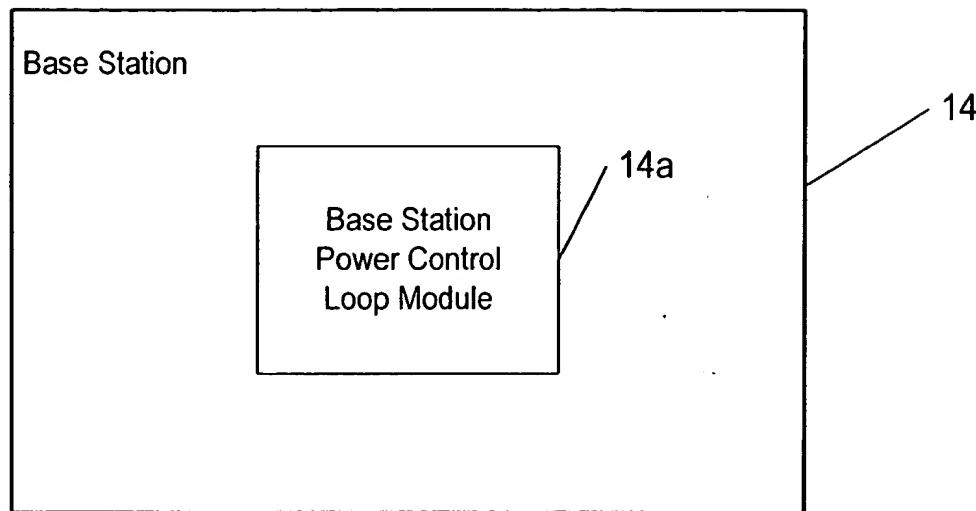


Figure 2 (The Base Station (Node B))

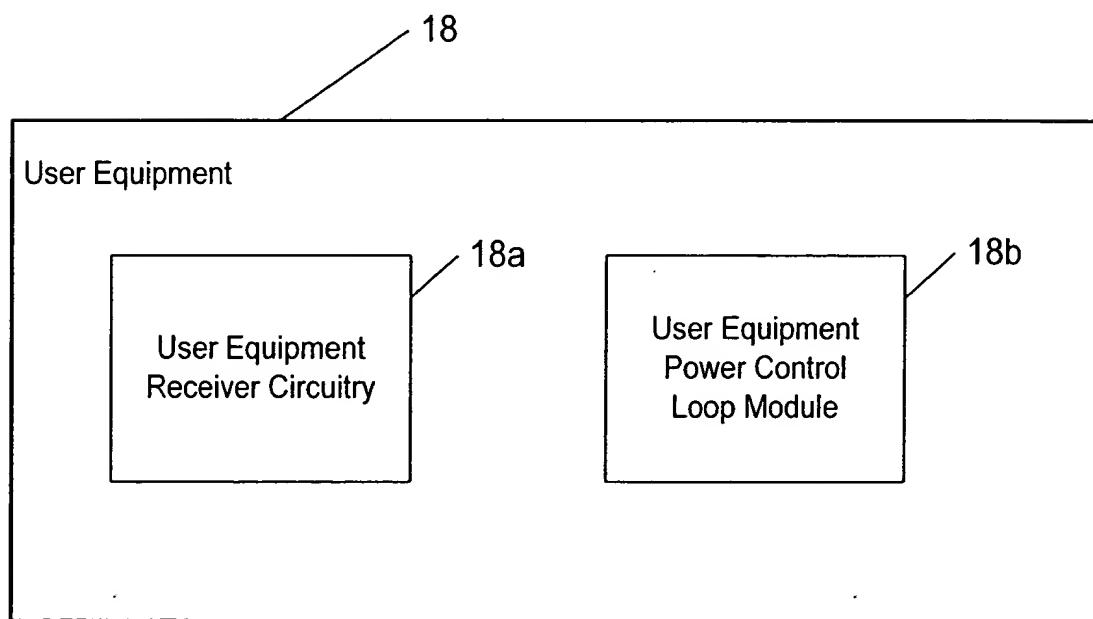


Figure 3 (The User Equipment)

TEST 80 "Executive"

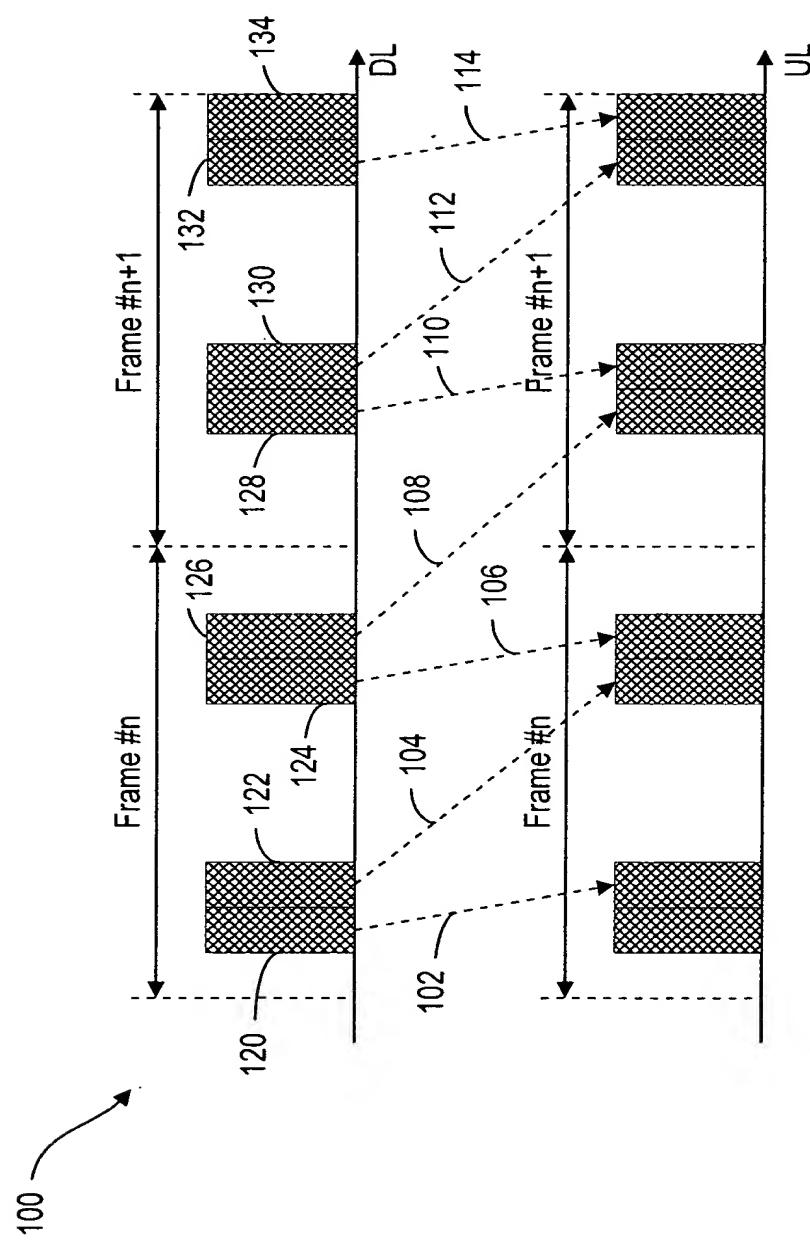


Figure 4 (Power Control Sequence)

"F D S T F 8 0 " G Z E D E G S 10

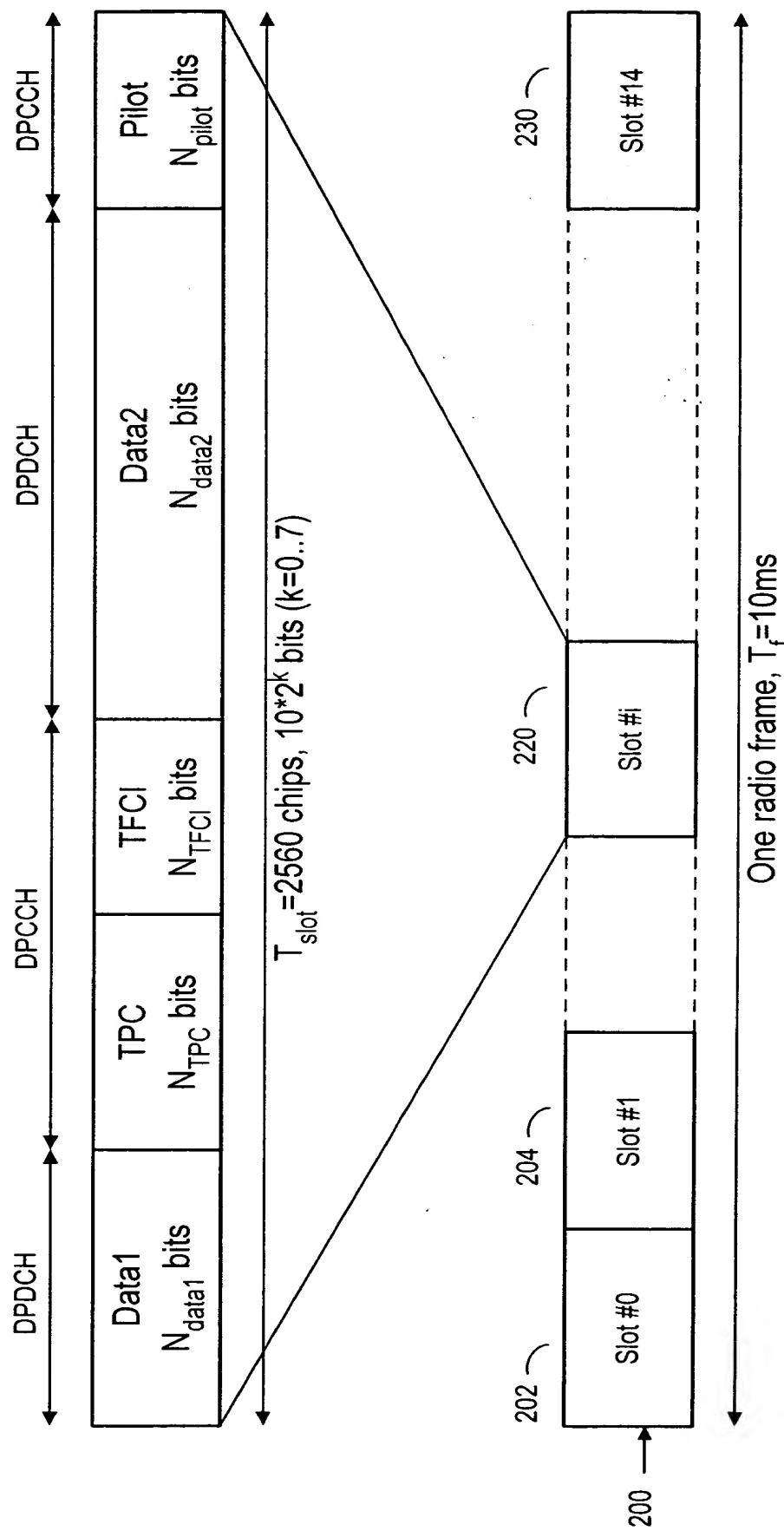


Figure 5 (The Frame Structure of the Downlink DPCH)

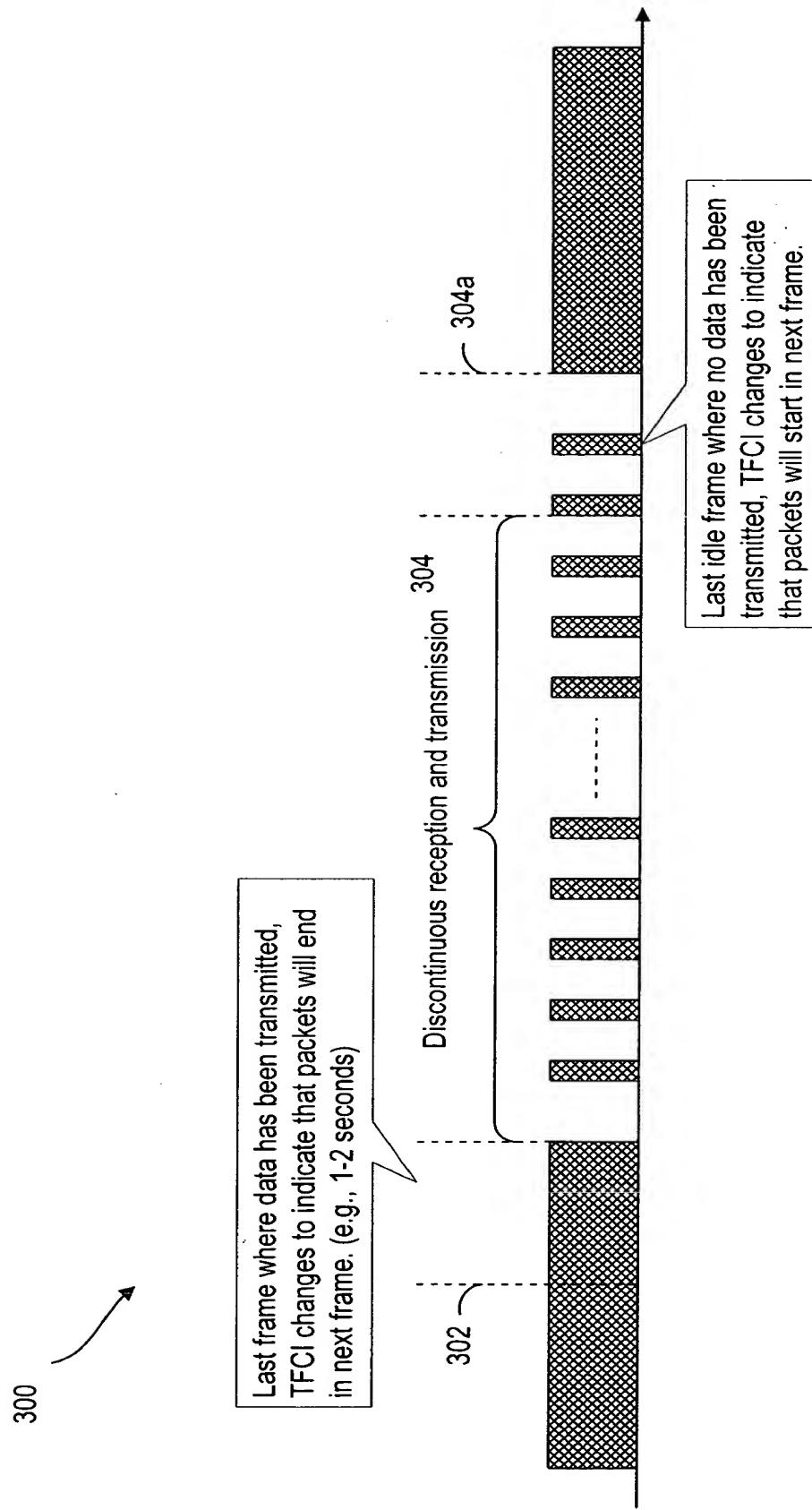


Figure 6 (The Usage of the TFCI Fields)

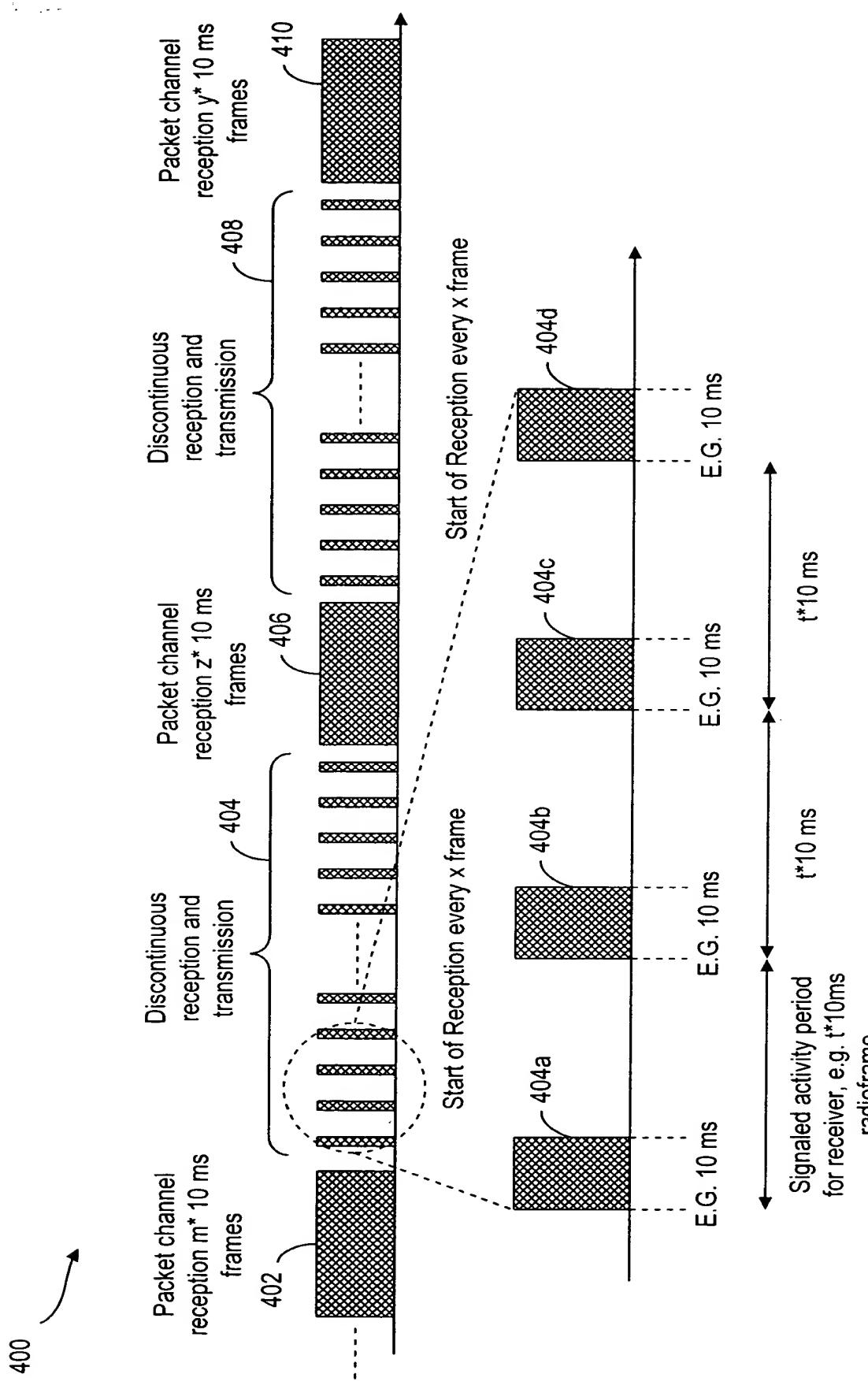


Figure 7 (The Higher Layer Signalling Scheme)

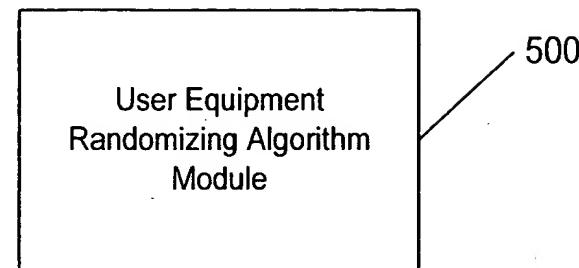


Figure 8  
(User Equipment Randomizing Algorithm Module)

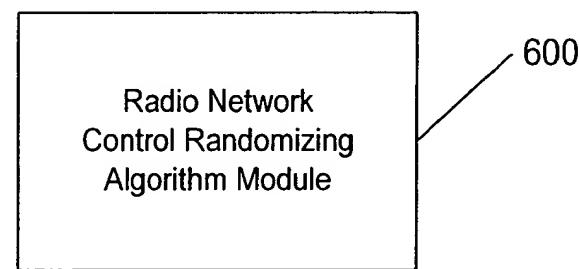


Figure 9  
(Radio Network Control Randomizing Algorithm Module)